

METHOD FOR REDUCING SPIKES IN A DIGITAL-TO-ANALOG CONVERTER

Abstract

A method is used for reducing spikes in a digital-to-analog converter (DAC), which includes a plurality of digit circuits for transforming a digital voltage signal into an analog voltage signal. The method includes receiving the digital voltage signal, setting the digit circuit corresponding to a predetermined bit of the digital voltage signal closest to an output module, and outputting an analog voltage signal corresponding to the digital voltage signal, wherein the predetermined bit of the digital voltage signal is the bit with least signal variation among the bits of the digital voltage signal.